



ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>Q2 83</p> <p>SCANNED  G.A. </p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/854146	CONT/PRIOR D	CLASS 438	SUBCLASS	ART UNIT 2825 <del>2812</del>	EXAMINER spek
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## APPLICANTS

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High accuracy timing model for integrated circuit verification

**TITLE**

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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